

# SystemVerilog Committee Meeting (Let Us Roll)



Vassilios Gerousis  
Accellera Technical Chairman  
Infineon Technologies

- 9:00 - 9:15 Introduction Vassilios Gerousis
- 9:15 - 10:15 SV committee status (all four committees).
  - SV Basic Johny Srouji / Karen Pieper
  - SV Assertion Faisal Haque.
  - SV C interface Swapnajt Mittra.
  - SV Enhancement David Smith.
- 10:15 - 10:30 Break
- 10:30 - 12:00 Synchronization, scheduling, SV 4.0 Peter Flake
- 12:00 - 12:30 Lunch
- 12:30 - 2:30 Technical Presentation (Technical Content)
  - Unified Assertion (SVA): Latest SVA LRM. (0.5 hour)
  - SVC (Direct-C, etc): Latest SVC LRM (0.5 hour)
  - Changes expected or proposed by basic (0.5 hour).
  - Enhancement and Testbench (SVT): Latest LRM and completed proposals.
- 2:30 - 2:45 Break
- 2:45 - 3:00 Patent Discussions ALL
- 3:00 - 4:00 Cadence Analysis of SV 3.0 Standard Jay Lawrence
- 4:00 - 4:30 Summary and Actions.

# SV committees accomplishment

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- All four committees have accomplished their milestones:
  - All donations have been accepted as the basis for SV 3.1 release.
  - LRM development has started (Testbench, CC, and assertion).
  - First assembly of SV 3.1 LRM
  - Clarification and improvement of SV 3.0 (Basic Committee).
- My deepest thanks for all participants!
- Our second Milestone, is fully assembled first draft (February).
- The chairs are making decisions to defer few things for next release of SV.

- Original patent is based on implementation of checkers.
- They have disclosed additional claims which are being filed at the patent office. They appear too general and not specific to our activities.
- We will continue with our activities within SystemVerilog and also PSL.
  - The 0-IN patent does not affect our standardization efforts.
  - 0-In refused to provide their technical assessment.
- Unless I hear otherwise from anyone, the 0-in patent is irrelevant to SystemVerilog and PSL LRMS.

- Released the first 3.1 draft 1.
- SystemVerilog Chairs and LRM Editors are all working in concert.
  - Our milestone to have a complete LRM 3.1 with ALL planned contents is still planned for End of February 2003.
  - In-Context review is an important event after February milestones.
  - Every committee is working hard to provide their portions for the 3.1 release.
- No decision has been made to call the LRM 3.1 or 4.0. This is still under discussion.

# Accellera Technical Committees



