Subject: PASSED (SystemVerilog) - ANSI-Style Parameter Port List BNF Correction

For your information, attached is the corrected ANSI-Style Parameter Port List BNF that passed in the April 15th SystemVerilog Conference call.

Mike McNamara has asked me to make sure this BNF correction is also issued as an IEEE errata when the IEEE Verilog Standards Group reconvenes sometime around the end of June.

I am still waiting for votes from the IEEE Verilog Synthesis Interoperability Group to include this same BNF in the pending Verilog Synthesis document that will shortly be sent out to ballot.

PASSED #1 - Correct the BNF for the Verilog-2001 ANSI-Style Parameter Port List as shown.

Regards - Cliff

PASSED:

A.1.3 Module and primitive source text (no change)

module_declaration ::=
    { attribute_instance } module_keyword module_identifier [module_parameter_port_list]
      [list_of_ports] **;** { module_item }
    endmodule
    | { attribute_instance } module_keyword module_identifier [module_parameter_port_list]
      [list_of_port_declarations] **;** { non_port_module_item }
    endmodule

A.1.4 Module parameters and ports (no change)

module_parameter_port_list ::= **# (** parameter_declaration { **,** parameter_declaration } **)**

A.1.5 Module items (added semicolons)

module_item ::=
    module_or_generate_item
    | port_declaration **;**
    | { attribute_instance } generated_instantiation
    | { attribute_instance } local_parameter_declaration
    | { attribute_instance } parameter_declaration **;**
    | { attribute_instance } specify_block
    | { attribute_instance } specparam_declaration

non_port_module_item ::=
    { attribute_instance } generated_instantiation
    | { attribute_instance } local_parameter_declaration
    | { attribute_instance } module_or_generate_item
    | { attribute_instance } parameter_declaration **;**
    | { attribute_instance } specify_block
    | { attribute_instance } specparam_declaration

A.2.1.1 Module parameter declarations (deleted semicolons)

parameter_declaration ::=
    parameter [**signed**] [range] list_of_param_assignments

| **parameter integer** <mark>list_of_param_assignments</mark>
    | **parameter real** <mark>list_of_param_assignments</mark>
    | **parameter realtime** <mark>list_of_param_assignments</mark>
    | **parameter time** <mark>list_of_param_assignments</mark>

A.2.3 Declaration lists (no change)

<mark>list_of_param_assignments ::= param_assignment { , param_assignment }</mark>

A.2.8 Block item declarations (added semicolon)

block_item_declaration ::=
    { attribute_instance } block_reg_declaration
  | { attribute_instance } event_declaration
  | { attribute_instance } integer_declaration
  | { attribute_instance } local_parameter_declaration
  | { attribute_instance } <mark>parameter_declaration ;</mark>
  | { attribute_instance } real_declaration
  | { attribute_instance } realtime_declaration
  | { attribute_instance } time_declaration